

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

MIKAEL SYLVEST, ET AL.

Application No.: 09/609,307

Filed: June 30, 2000

For: **Method and Apparatus for Avoiding
Transient Loops During Network Topology
Adoption**



Art Group: 2756

Examiner: Not Assigned

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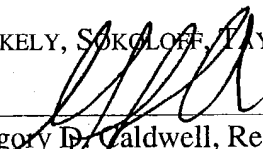
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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

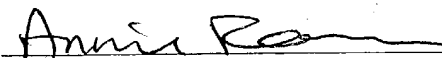
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if Known

Application Number	09/609,307
Filing Date	June 30, 2000
First Named Inventor	Mikael Sylvest
Art Unit	2756
Examiner Name	Not Assigned
Attorney Docket Number	42390P8719

Sheet

1

of

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	1	US-5,276,789 A	01-04-1994	Besaw et al.	
	2	US-5,481,674 A	01-02-1996	Mahavadi	
	3	US-5,606,664 A	02-25-1997	Brown et al.	
	4	US-5,606,669 A	02-25-1997	Bertin et al	
	5	US-5,706,440	01-06-1998	Compliment et al.	
	6	US-5,727,157	03-10-1998	Orr et al.	
	7	US-5,732,086	03-24-1998	Liang et al.	
	8	US-5,734,824	03-31-1998	Choi	
	9	US-5,825,772	10-20-1998	Dobbins et al	
	10	US-5,884,036 A	03-16-1999	Haley	
	11	US-6,061,723 A	05-09-2000	Walker et al.	
	12	US-6,122,283 A	09-19-2000	Lee	
	13	US-6,188,675 B1	02-13-2001	Casper et al.	
	14	US-6,205,122 B1	03-20-2001	Sharon et al.	
	15	US-6,456,599 B1	09-24-2002	Elliott	
	16	US-6,590,861	07-08-2003	Vepa et al.	
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FOREIGN PATENT DOCUMENTS

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STATEMENT BY APPLICANT**

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Application Number	09/609,307
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First Named Inventor	Mikael Sylvest
Art Unit	2756
Examiner Name	Not Assigned
Attorney Docket Number	42390P8719

Sheet	2	of	2
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NON PATENT LITERATURE DOCUMENTS

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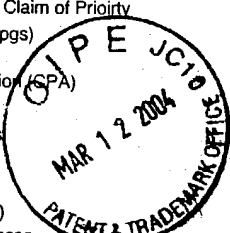
Application No: 09/609,307 Filing Date: 06/30/00 BSTZ Docket #: 42390.P8719 Atty/Sec: EHT/GDC/RAD/agg
Date Mailed: 03/09/2004 Docket Due Date(s): _____ Client: INTEL CORPORATION
Title Method and Apparatus for Avoiding Transient Loops During Network Topology Adoption

Inventor(s) Sylvest Heilmann

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(___ pages w/ cover & abstract) | <input type="checkbox"/> Nonpublication Request Under 35 U.S.C. 122(b)(2)(B)(i) |
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